IP Link® Device Interface Communication Sheet

This document provides additional assistance with wiring your Extron IP Link enabled product to your device. Different components may require a different wiring scheme than those listed below.

For complete operating instructions, refer to the user's manual for the specific Extron IP Link enabled product or the controlled device manufacturer supplied documentation.

Device Specifications:

Device Type:	Audio Processor
Manufacturer:	Atlona
Firmware Version:	N/A
Model(s):	AT-PA100

Version History:

Driver Version	Date	IP Link Compiler	GC Version	Notes
1	7/15/2011	1.4.0	3.2.0	Initial version.

Driver Notes:

Control Commands & States:

Audio Mute	On	Off
Bass (Discrete)	0 to 8 in steps of 1	
Bass (Step)	Up	Down
Input	1	2
Line (Step)	Up	Down
Line Level	0 to 60 in steps of 1	
Mic Level	0 to 60 in steps of 1	
Mic Level (Step)	Up	Down
Mute	Mic	Line
Reset	Execute	
Treble (Discrete)	0 to 8 in steps of 1	
Treble (Step)	Up	Down

Status Available:

Connection Status	Connected	Disconnected
Input	1	2

MLC62 Supported Commands:

Audio Mute	On	Off
Bass (Step)	Up	Down
Input	1	2
Line (Step)	Up	Down
Mic Level (Step)	Up	Down
Mute	Mic	Line
Reset	Execute	
Treble (Step)	Up	Down

Cable and Adapter Requirements:

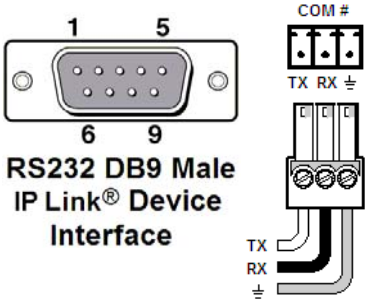
Please see wiring diagram below.

Notes for the Device:

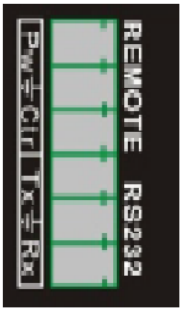
Serial communication:

Port Type:	RS-232	**Parity:**	None
Baud Rate:	9600	**Stop Bits:**	1
Data Bits:	8	**Flow Control:**	None

Pin Assignments Diagram:



Signal	Pin	Main Cable	Signal
TxD	2	←	TxD
RxD	3	→	RxD
GND	5	—	⏚



Note: Captive screw connector may also be used as a serial connection.

General Notes: